THIRD YEAR:ELECTRONICS AND COMMUNICATION ENGINEERING

SCHEME OF INSTRUCTION AND EXAMINATION

(RC 2016-17)

SEMESTER – V

Subject Code	Name of the Subject	Scheme of Instruction Hrs/Week			Scheme of Examination						
		L	T	P#	ThDur (Hrs)	Marks					
						Th	S	TW	P	O	Total
ETC/ECE 5.1	Digital Signal Processing	3	1	2	3	100	25	25	--	--	150
ETC/ECE 5.2	Transmission Lines and	3	1	--	3	100	25	--	--	--	125
ETC/ECE 5.3	Control Systems Engineering	3	1	--	3	100	25	--	--	--	125
ETC/ECE 5.4	Embedded Systems	3	1	2	3	100	25	--	25	--	150
ETC/ECE 5.5	VLSI Design and Technology	4	--	2	3	100	25	--	25	--	150
ETC/ECE 5.6	Analog Communication	4	--	2	3	100	25	--	--	25	150
	TOTAL	**20**	**4**	**8**		**600**	**150**	**25**	**50**	**25**	**850**

#A candidate is considered to have successfully fulfilled the requirement of a semester, provided he/ she submits to the department a certified journal reporting the experiments conducted during the semester.

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SEMESTER – VI

Subject Code	Name of the Subject	Scheme of Instruction Hrs/Week			Scheme of Examination						
		L	T	P#	ThDur (Hrs)	Marks					
						Th	S	TW	P	O	Total
ETC/ECE 6.1	Electronic System Design and Manufacturing	4	--	2	3	100	25	25	--	--	150
ETC/ECE 6.2	High Performance Computing Architectures	4	--	2	3	100	25	--	--	--	125
ECE 6.3	Digital Communication and Coding	3	1	--	3	100	25	--	--	25	150
ETC/ECE 6.4	Industrial Automation and Instrumentation	3	1	2	3	100	25	--	25	--	150
ETC/ECE 6.5	Operating Systems	3	1	--	3	100	25	--	--	--	125
ETC/ECE 6.6	Communication Networks	3	1	2	3	100	25	--	25	--	150
	TOTAL	**20**	**4**	**8**		**600**	**150**	**25**	**50**	**25**	**850**