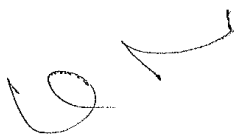


APPROVED BY BOS CHAIRMAN

GOA UNIVERSITY
THIRD YEAR OF BACHELOR'S DEGREE COURSE IN ELECTRONICS
AND COMMUNICATION ENGINEERING

SCHEME OF INSTRUCTION AND EXAMINATION
SEMESTER V

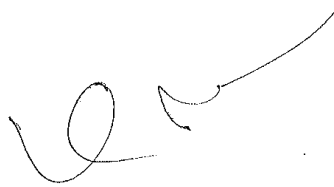
Sub code	Subjects	Scheme Of Instruction Hrs/Week			Scheme Of Examination					
		L	T	P	Th. Dur (Hrs)	Marks				
						Th.	S	P	O	Total
5.1	Probability Theory and Random Processes	4	0	-	3	100	25	-	-	125
5.2	Control System Engineering	4	0	2	3	100	25	-	-	125
5.3	Communication Engineering-I	4	0	2	3	100	25	-	50	175
5.4	Microprocessors	4	0	2	3	100	25	-	50	175
5.5	Digital Signal Processing	4	0	2	3	100	25	-	-	125
5.6	Transmission Lines and Waveguides	4	0	-	3	100	25	-	-	125
	Total	24	0	8		600	150		100	850


PROFESSOR & HEAD
Dept. of Electronics & Telecommunication Engineering
Padra Cencelcao College of Engineering
Agnel Technical Complex
VERNA - GOA PIN: 403 722

SEMESTER VI

Sub code	Subjects	Scheme Of Instruction Hrs/Week			Scheme Of Examination					
		L	T	P	Th. Dur.	Marks				
						Th.	S	P	O	Total
6.1	Communication Engineering-II	4	0	0	3	100	25	-	50	175
6.2	Introduction to Embedded Systems Design	4	0	2	3	100	25	-	-	125
6.3	Power Electronics	4	0	2	3	100	25	-	-	125
6.4	Antenna and Wave Propagation	4	0	-	3	100	25	-	-	125
6.5	Electronic Instrumentation	4	0	2	3	100	25	-	-	125
6.6	VLSI Technology and Design	4	0	2	3	100	25	-	50	175
Total		24	0	8		600	150		100	850

L – Lectures, T-Tutorials, P-Practicals. Th. Dur. – Duration of Theory Paper
 Th – Theory, S – Sessional, P– Practical, O – Oral.



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