SECOND YEAR: ELECTRONICS AND

COMMUNICATION ENGINEERING

SCHEME OF INSTRUCTION AND EXAMINATION

(RC 2016-17)

<u>SEMESTER – III</u>

Subject Code	Name of the Subject	Scheme of Instruction Hrs/Week			Scheme of Examination						
		L	Т	P#	ThDur (Hrs)	Th	S	Mai TW	rks P	0	Total
ETC/ECE 3.1	Applied Mathematics-III	4			3	100	3 25				125
ETC/ECE 3.2	Economics and Management	4			3	100	25				125
ETC/ECE 3.3	Algorithms for Data Structures	3	1	2	3	100	25	25			150
ETC/ECE 3.4	Electronic Devices and Circuits –I	3	1	2	3	100	25		25		150
ETC/ECE 3.5	Digital System Design	3	1	2	3	100	25		25		150
ETC/ECE 3.6	Electrical Circuits and Systems	3	1	2	3	100	25			25	150
	TOTAL	20	4	8		600	150	25	50	25	850

#A candidate is considered to have successfully fulfilled the requirement of a semester, provided he/ she submits to the department a certified journal reporting the experiments conducted during the semester.

SECOND YEAR: ELECTRONICS AND COMMUNICATION ENGINEERING

SCHEME OF INSTRUCTION AND EXAMINATION

(RC 2016-17)

SEMESTER – IV

Subject Code	Name of the Subject	Scheme of Instruction Hrs/Week			Scheme of Examination							
		L	Т	P#	ThDur (Hrs)	Marks						
						Th	S	тw	Р	0	Total	
ETC/ECE 4.1	Probability Theory and Random	4			3	100	25				125	
ETC/ECE 4.2	Signals and Systems	3	1		3	100	25				125	
ETC/ECE 4.3	Electromagnetic Fields and Waves	4		2	3	100	25	25			150	
ETC/ECE 4.4	Electronic Devices and Circuits –II	3	1	2	3	100	25		25		150	
ETC/ECE 4.5	Linear Integrated Circuits	3	1	2	3	100	25		25		150	
ETC/ECE 4.6	Microprocessors and Interfacing	3	1	2	3	100	25			25	150	
	TOTAL	20	4	8		600	150	25	50	25	850	

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