

APPROVED BY BOS CHAIRMAN

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GOA UNIVERSITY

SECOND YEAR OF BACHELOR'S DEGREE COURSE IN
ELECTRONICS AND COMMUNICATION ENGINEERING
SCHEME OF INSTRUCTION AND EXAMINATION

SEMESTER III.

Sub code	Subjects	Scheme Of Instruction Hrs/Week			Scheme Of Examination					
		L	T	P	Th. Dur (Hrs)	Marks				
						Th.	S	P	O	Total
3.1	Applied Mathematics-III	4	0	-	3	100	25	-	-	125
3.2	Digital System Design	4	0	2	3	100	25	50	-	175
3.3	Network Analysis and Synthesis	3	1	2	3	100	25	-	-	125
3.4	Electronic Devices and Circuits	3	1	2	3	100	25	50	-	175
3.5	Managerial Economics	4	0	-	3	100	25	-	-	125
3.6	Introduction to Java	4	0	2	3	100	25	-	-	125
	Total	22	2	8		600	150	100		850

L – Lectures, T-Tutorials, P-Practicals.

Th. Dur. – Duration of Theory Paper

Th – Theory, S – Sessional, P– Practical, O – Oral.

Handwritten signature

PROFESSOR & HEAD
Dept of Electronics & Telecommunication Engineering
Padre Conceicao College of Engineering
Agnel Technical Complex
VERNA - GOA PIN: 403 722

Received
23/7/12

SEMESTER IV.

Sub code	Subjects	Scheme Of Instruction Hrs/Week			Scheme Of Examination					
		L	T	P	Th. Dur (Hrs)	Marks				
						Th.	S	P	O	Total
4.1	Applied Mathematics-IV	4	0	-	3	100	25	-	-	125
4.2	Signals and Systems	3	1	2	3	100	25	-	50	175
4.3	Electrical Technology	4	0	2	3	100	25	-	-	125
4.4	Electro magnetic Fields and Waves	3	1	-	3	100	25	-	-	125
4.5	Linear Integrated circuits	4	0	2	3	100	25	50	-	175
4.6	Data structures using C++	4	0	2	3	100	25	-	-	125
	Total	22	2	8		600	150	50	50	850

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P-Practicals. Th. Dur. –
Duration of Theory Paper
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